1. Open terminal and type-

iverilog –o sim improv\_tb.v improv.v

vvp sim

1. Open the output file c.vcd using gtk wave and insert variables.
2. The output q is the quotient binary after decimal pt. for eg: in case of x= 3 , y=8, q = 01100000 then the quotient is 0.01100000
3. Remaider value is displayed by r. It is a signed value.
4. To change input, change x, y in test bench file.
5. Bit count of input and output can be changed in improv.v